

IN THE SPECIFICATION

Please amend the Specification as follows:

In the abstract please replace the first paragraph with the following rewritten paragraph:

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<sup>1</sup>  
B The invention relates to a method and an arrangement for instruction word generation in the controlling of functional units in a processor, the instruction words comprising a plurality of instruction word parts. In this case, in a program sequence, under the control of a program word, an instruction word is taken from a row – determined by a reading row number – of an instruction word memory that can be written to row by row, the said instruction word is altered by means of substitution of an instruction word part by the information part of the respective program word and is written back to a row of the instruction word memory, the said row being determined by a writing row number. Afterwards, an instruction word – which is generated in this way and is to be executed in accordance with the program – for controlling the functional units is output to the processor.

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<sup>2</sup>  
B Please amend the title with the following rewritten title:

METHOD AND ARRANGEMENT FOR INSTRUCTION WORD GENERATION IN  
THE CONTROLLING OF FUNCTIONAL UNITS IN A PROCESSOR

Please replace paragraph 1 with the following rewritten paragraph:

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<sup>2</sup>  
B The invention relates to a method for the generation of instruction words for driving functional units in a processor, the instruction words comprising a plurality of instruction word parts. Each instruction word part respectively controls a functional unit. A sequence of primary instruction words, which originate from a translation of a program code, undergoes fractionation

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into program words of a program sequence. During the program sequence, under the control of a program word which has an information part at least of the width of an instruction word part, an instruction word is taken from a row – determined by a reading row number – of an instruction word memory that can be written to row by row and is altered by means of substitution of an instruction word part with the information part of the respective program word. It is then written back to a row of the instruction word memory, the said row being determined by a writing row number. After generation – effected in this way – of an instruction word corresponding to the primary instruction word to be executed, the instruction word is provided to an output for driving the functional units.

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Please replace paragraph 3 with the following rewritten paragraph:

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The German Patent Specification DE 198 59 389 C1 describes an arrangement for controlling functional units in a processor. As discussed therein, the program word contains, in addition to an information part, at least also the information about the writing and reading row numbers for the instruction word memory. This necessitates a large width of the program words which, on the one hand, with the requisite processing and decoding of the control information, produce limitations in the processing speed of the task-related data. On the other hand, the high data width of the program word processing necessitates high outlay on hardware in the realization of the processor.

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